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Lab 5 Report

So I fixed a mistake during lab and I wanted to explain it because the pre lab code doesn’t have this change. The only change was this line in the ctrl1.vhd and ctrl2.vhd. The original line had the first state be INIT (the changed code is in the pictures below). I must’ve changed the names of my states or somehting and in the middle of it all forgot to change that. Without this change, the FSM\_D1 and FSM\_D2 I passed all the tests in the test bench except the first test. I didn’t really pay much attention to it because it worked on my board and I had a ton of other work to do for other classes. However, after this change, I pass all of the tests.



